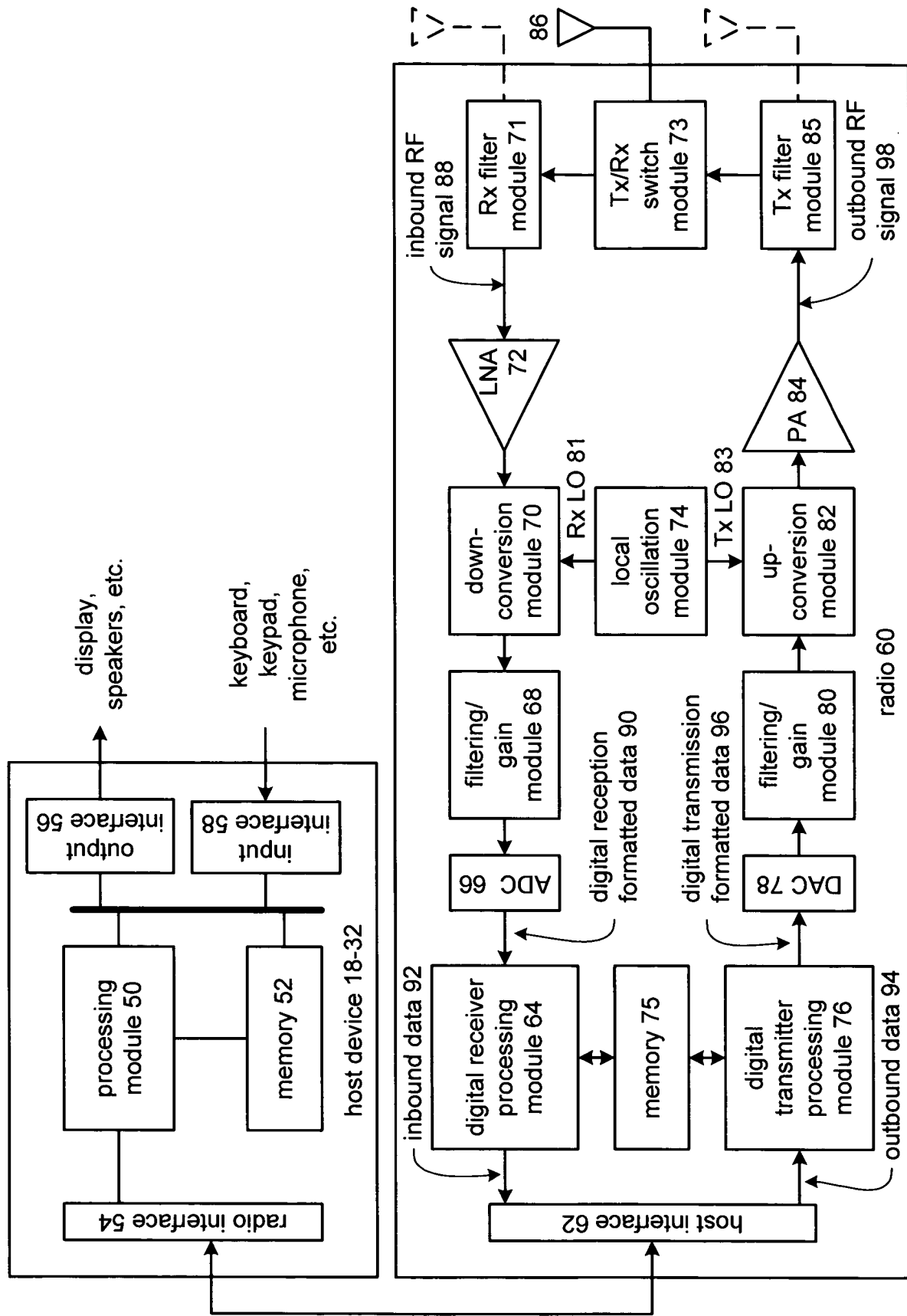
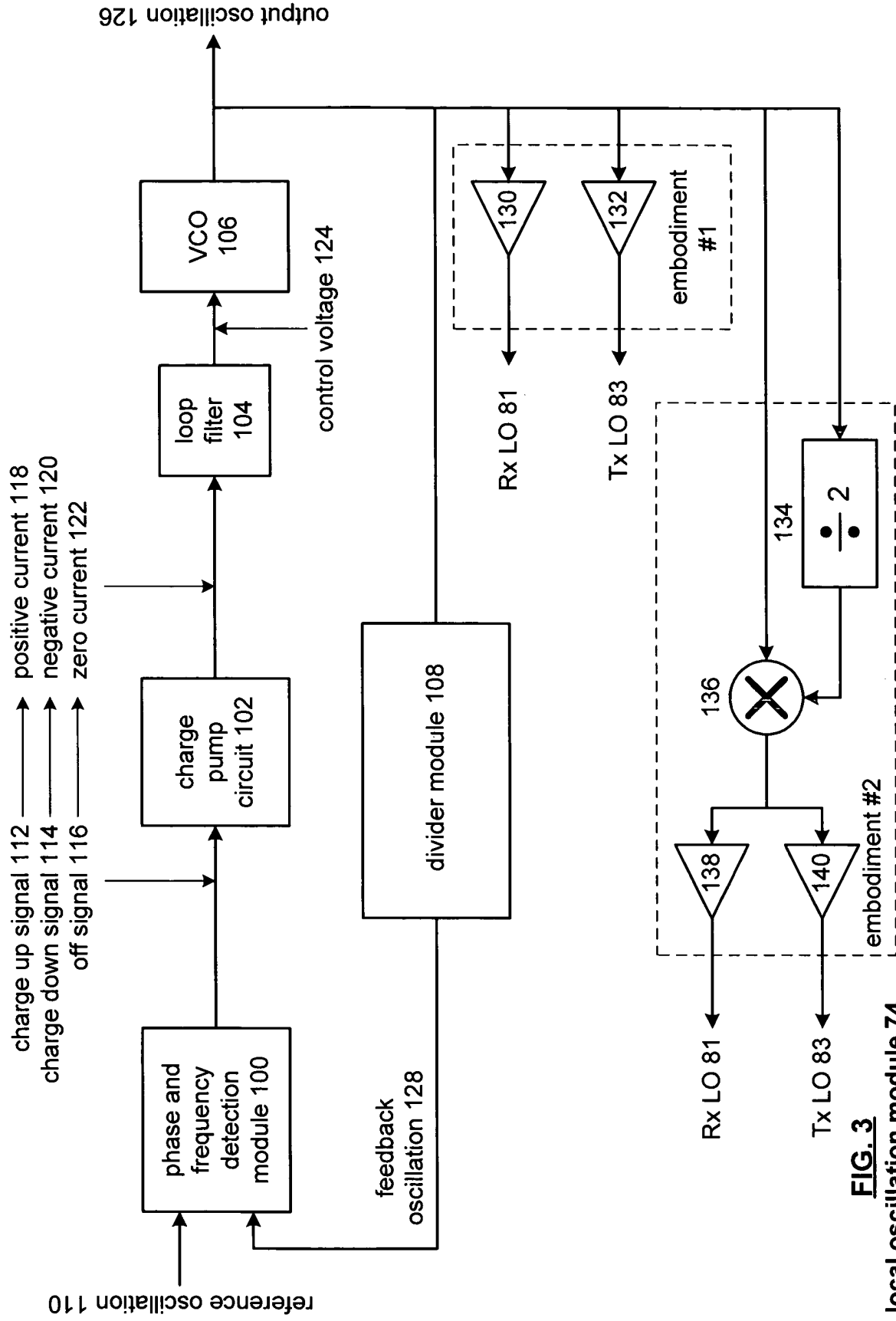


**FIG. 1**

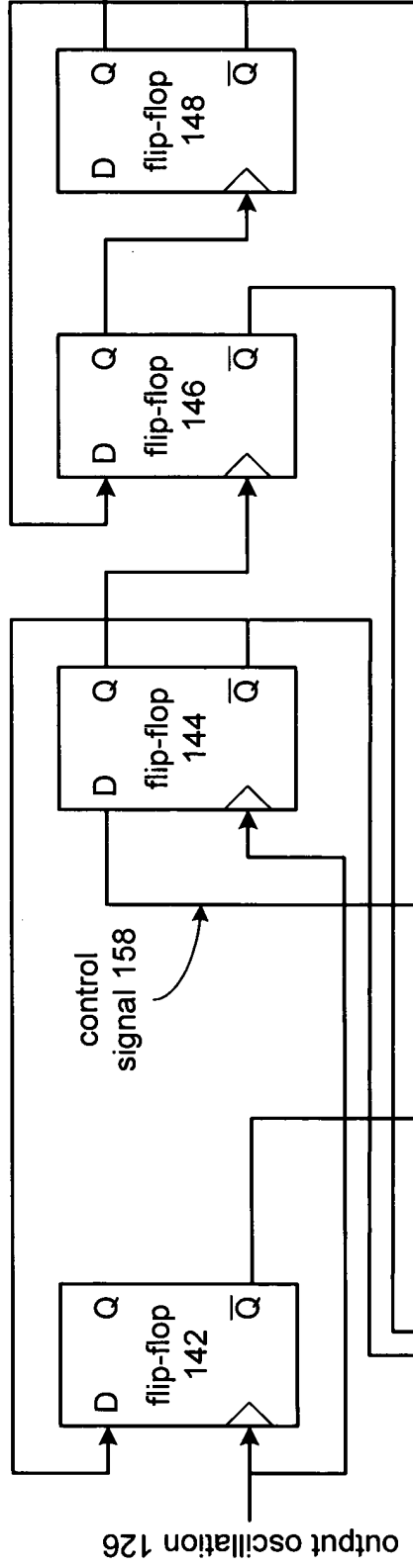


**FIG. 2**



**FIG. 3**  
local oscillation module 74

feedback oscillation 128

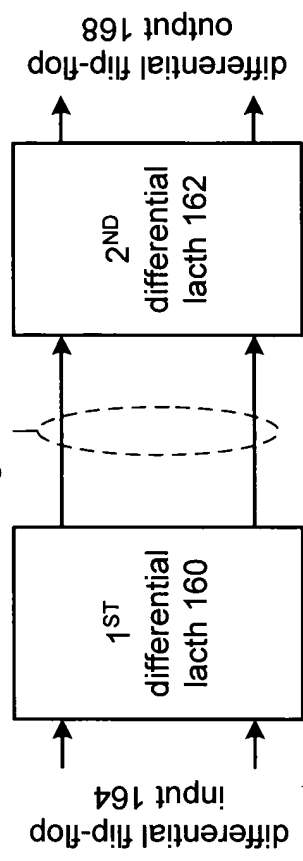


divider select signal 152

control  
signal 158

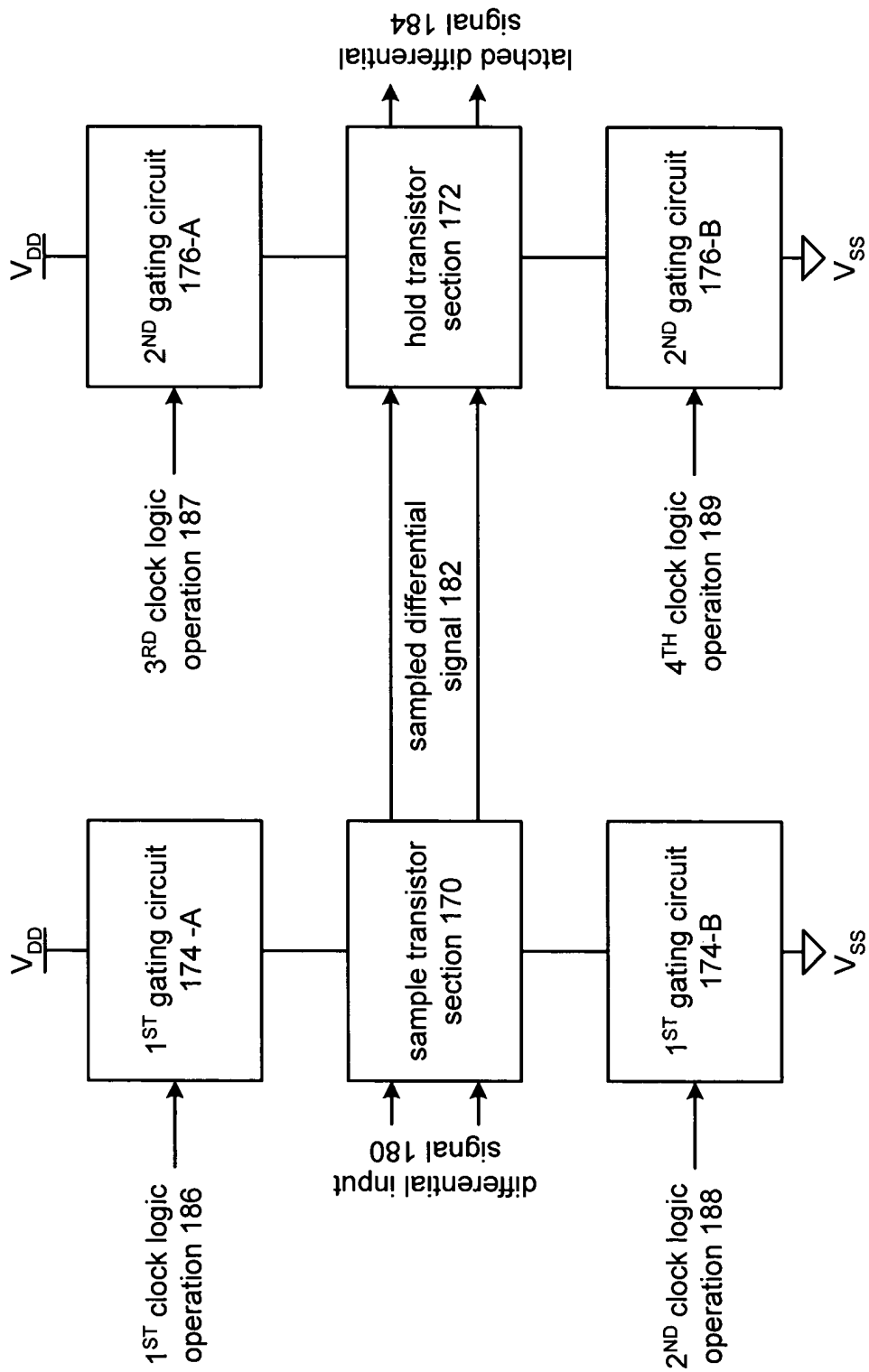
output oscillation 126

differential latched  
signal 166

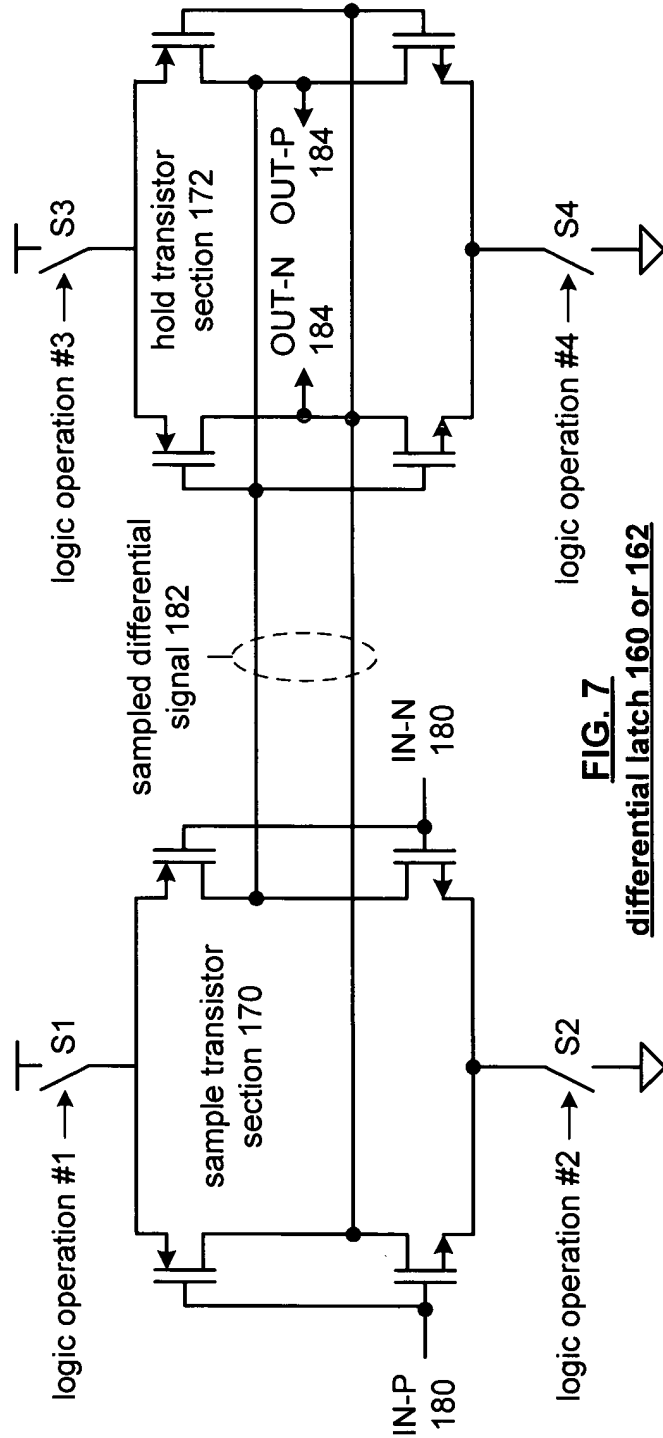


**FIG. 5**  
flip-flop 142 - 148

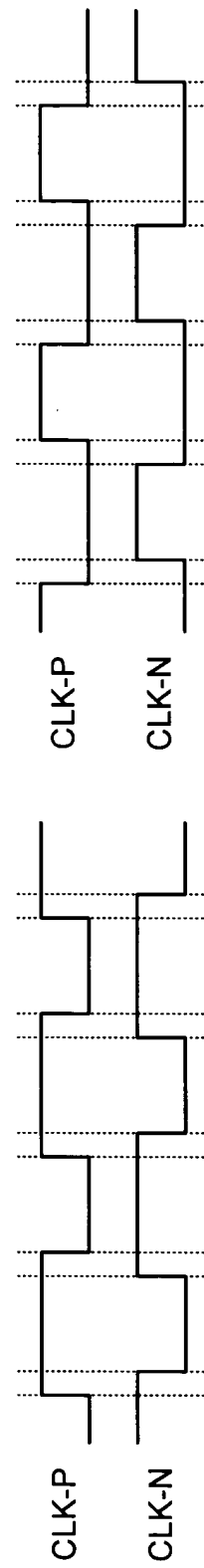
**FIG. 4**  
divider module 108



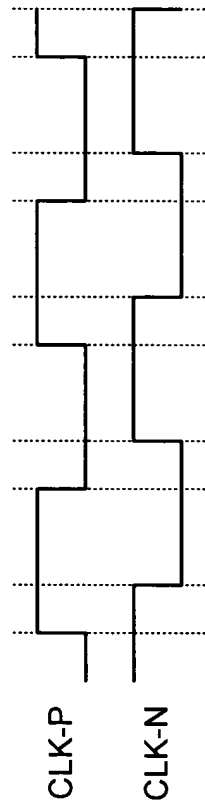
**FIG. 6**  
differential latch 160 or 162



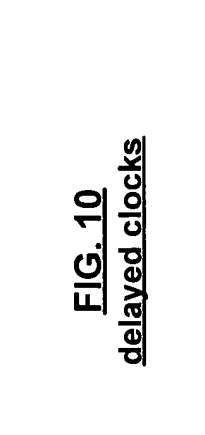
**FIG. 7**  
differential latch 160 or 162



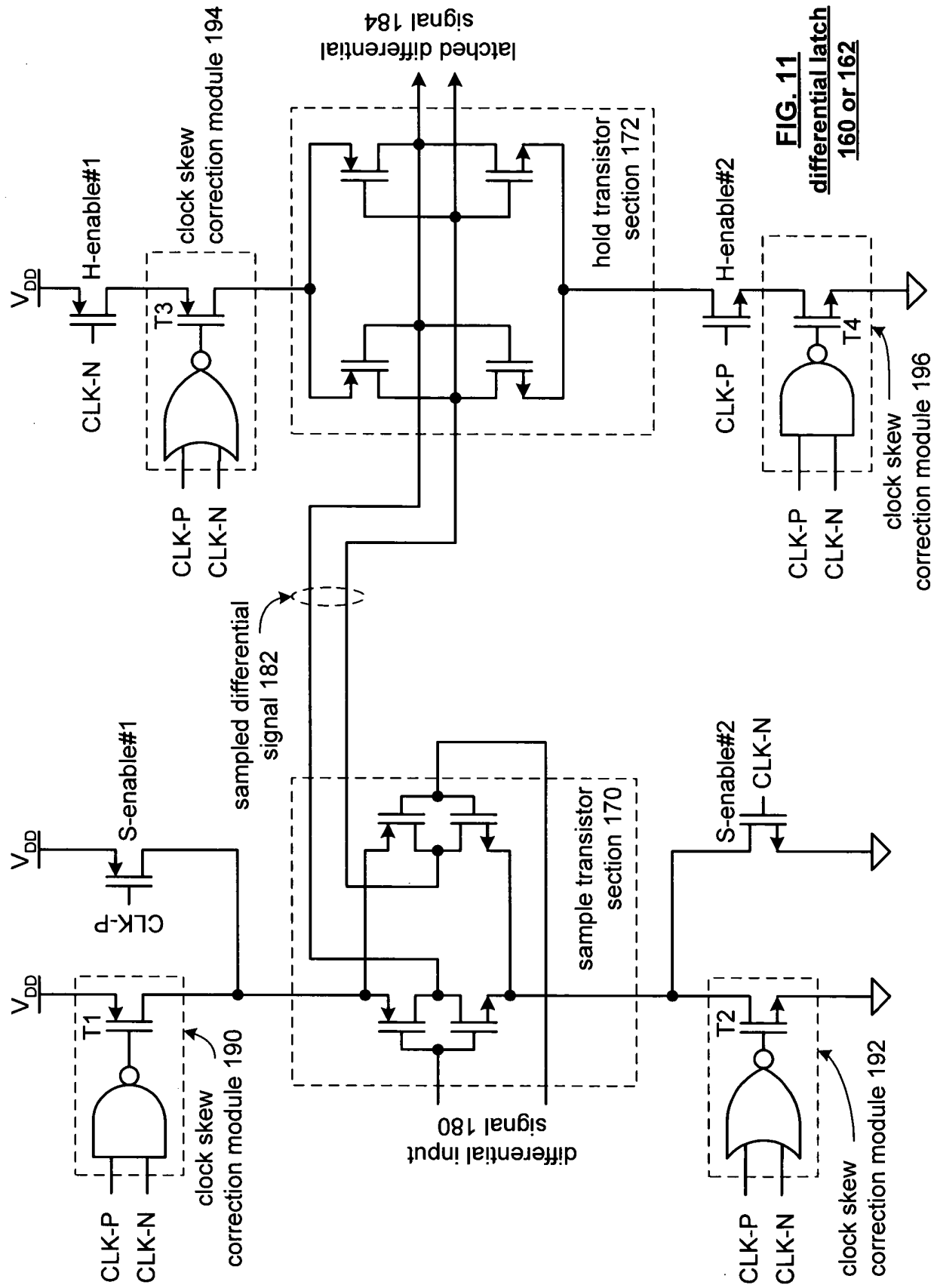
**FIG. 8**  
overlapping clocks

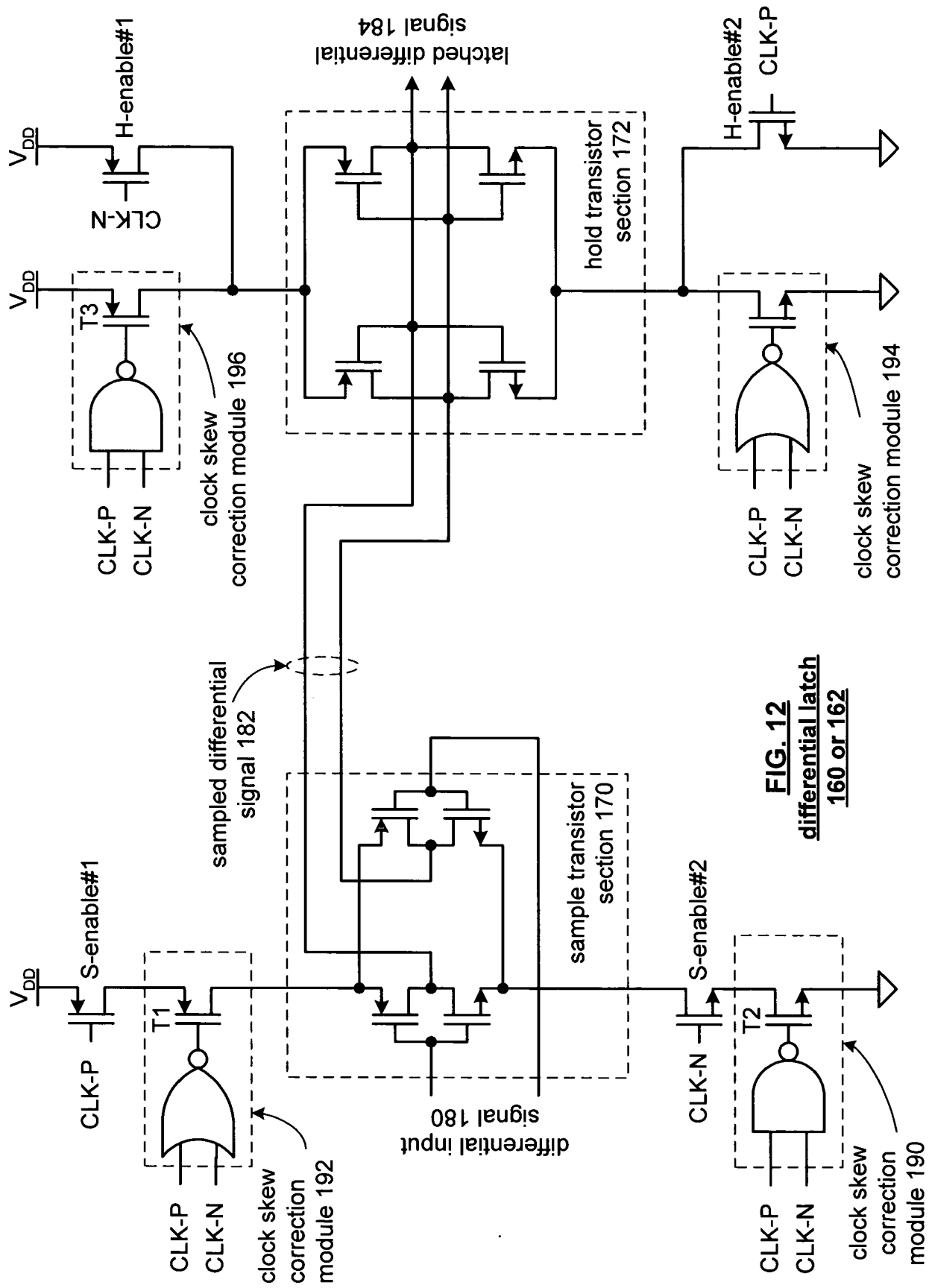


**FIG. 9**  
non-overlapping clocks



**FIG. 10**  
delayed clocks





**FIG. 12**  
**differential latch**  
**160 or 162**